

PATENT APPLICATION

Atty. Docket No.: 9898-172 Serial No.: 09/773,874
 Applicant: Young-Sik KIM, et al.
 Filing Date: January 31, 2001 Group: 2111

**INFORMATION DISCLOSURE CITATION
 FORM PTO-1449 (Modified)**

U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub</u> <u>Class</u>
<u>DSKm</u>		5,528,767	Jun. 18, '96	Chen	7	7
		5,691,745	Nov. 25, '97	Mital	7	7
		6,125,412	Sep. 26, '00	Picard, et al.	7	7

FOREIGN PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
<u>DSKm</u>		EP0479702	Apr. 8, '92	Europe	Boldt, et al.
		EP0518504	Dec. 16, '92	Europe	Fuoco, et al.
		EP0564116	Oct. 6, '93	Europe	Drerup, et al.
		EP0592213	Apr. 13, '94	Europe	Chester A. Pawlowski
		FR2674361	Sep. 25, '92	France	Chasserieau, et al.
		WO96/41325	Dec. 19, '96	PCT	Chee, et al.
		WO97/06523	Feb. 20, '97	PCT	G.R. Mohan Rao
		WO97/26604	Jul. 24, '97	PCT	Fu-Chieh Hsu

OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, etc.</u>
<u>DSKm</u>		English language abstract of French Publication No. FR2674361
		Bursky, D., "486 CPU Packs Peripherals for Real-Time System Needs," Electronic Design, vol. 43, no. 18, 5 Sep. 1995, pp. 167-168
		Wingard, D., et al., "Integration Architecture for System-On-A-Chip Design," Custom Integrated Circuits Conference, IEEE, 11 May 1998, pp. 85-88

Examiner: Donald K. Mason

Date Considered: 8-5-2005